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#
# Confsim file created on: 02/24/04 11:45:49
#
# Tiger tester
#
#         is a 2 processor system
# Processor 1: 450 MHz sparcv9 (online)
# Processor 3: 450 MHz sparcv9 (online)
# PCI based system
# Terabus is present
#   Board ID      949-824-00
#   Serial number
# TCI is present
#

```

TIGER_TH 1
BACKPLANE A

#Slot	Type	Serial #	Rev	Num	Date	XptA	XptB	Name
2	000-000-00	0x00000000	P0	0	# 0000-0	23	24	EMPTY
3	000-000-00	0x00000000	P0	0	# 0000-0	21	22	EMPTY
4	000-000-00	0x00000000	P0	0	# 0000-0	19	20	EMPTY
5	000-000-00	0x00000000	P0	0	# 0000-0	17	18	EMPTY
6	000-000-00	0x00000000	P0	0	# 0000-0	15	16	EMPTY
7	000-000-00	0x00000000	P0	0	# 0000-0	13	14	EMPTY
8	000-000-00	0x00000000	P0	0	# 0000-0	11	12	EMPTY
9	000-000-00	0x00000000	P0	0	# 0000-0	9	10	EMPTY
10	000-000-00	0x00000000	P0	0	# 0000-0	7	8	EMPTY
11	000-000-00	0x00000000	P0	0	# 0000-0	5	6	EMPTY
14	000-000-00	0x00000000	P0	0	# 0000-0	25	26	EMPTY
15	000-000-00	0x00000000	P0	0	# 0000-0	27	28	EMPTY
16	000-000-00	0x00000000	P0	0	# 0000-0	29	30	EMPTY
17	000-000-00	0x00000000	P0	0	# 0000-0	31	32	EMPTY
18	000-000-00	0x00000000	P0	0	# 0000-0	33	34	EMPTY
19	000-000-00	0x00000000	B	0	# 0000-0	35	36	EMPTY
20	000-000-00	0x00000000	P0	0	# 0000-0	37	38	EMPTY
21	000-000-00	0x00000000	P0	0	# 0000-0	39	40	EMPTY
22	949-894-00	0x029fd03	B	0	# 0306-0	41	42	QVS CC
23	000-000-00	0x00000000	P0	0	# 0000-0	43	44	EMPTY
1	949-886-00	0x02a25e6	B	0	# 0231-0	3	0	ACISB-L
24	949-886-01	0x02a1f4a	B	0	# 0231-0	4	0	ACISB-R

END

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#
# Up to 4 Precision AC Card Cages are allowed
#

```

PRECISION_AC 1

#Slot	Type	Serial #	Rev	Num	Date	Name
1	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
2	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
3	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
4	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
5	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
6	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
7	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
8	949-671-20	0x02a2404	E	0	# 0014-0	PACS CAGE INT

END

PRECISION_AC 2

#Slot	Type	Serial #	Rev	Num	Date	Name
1	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
2	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
3	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
4	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
5	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
6	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
7	000-000-00	0x00000000	P0	0	# 0000-0	EMPTY
8	949-671-20	0x02a2401	E	0	# 0014-0	PACS CAGE INT

END

#

Up to 8 Universal Backplane/Synch Power Subsystem

cages are allowed

#

For the Synch Power Subsystem:

# Slot	Type	Name	Instr1	#	Instr2	#	Ammeter	#
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#

Instr1 # - instrument connected to the first two matrix lines

Instr2 # - instrument connected to the last two matrix lines

Ammeter # - ammeter connection

to AVOID errors, put NO 0 if no instrument is connected.

#

#

UB_SPS_CAGE 1

# Slot	Type	Serial #	Code	Num	Date	Name
1	879-802-02	0x029e65f	F	0	# 9817-0	UB_SPS_802
2	517-301-00	0x0282bfe	B	0	# 0229-0	UB_APU
3	517-301-00	0x02831d0	B	0	# 0229-0	UB_APU
4	517-301-00	0x0282c00	B	0	# 0229-0	UB_APU
5	517-301-00	0x0282bff	B	0	# 0229-0	UB_APU
6	517-301-00	0x026ec04	B	0	# 0229-0	UB_APU
7	517-301-00	0x0282505	B	0	# 0229-0	UB_APU
8	517-301-00	0x02831da	B	0	# 0229-0	UB_APU
9	517-301-00	0x0280ec4	B	0	# 0229-0	UB_APU
10	517-301-00	0x0282bec	B	0	# 0229-0	UB_APU
11	517-301-00	0x0282c19	B	0	# 0229-0	UB_APU
12	517-301-00	0x0282bfd	B	0	# 0229-0	UB_APU
13	517-301-00	0x02831c1	B	0	# 0229-0	UB_APU
14	879-925-01	0x029f878	D	0	# 0317-0	UB_60_V_SRC MAT 1
15	879-925-01	0x029e488	D	0	# 0317-0	UB_60_V_SRC DUT 1
16	879-925-01	0x029f883	D	0	# 0317-0	UB_60_V_SRC MAT 2
17	879-925-01	0x029f896	D	0	# 0317-0	UB_60_V_SRC MAT 3
18	879-925-01	0x029e46c	D	0	# 0317-0	UB_60_V_SRC MAT 4
21	879-690-00	0x02a1345	B	0	# 0128-0	UB_ASY
22	517-300-01	0x0283051	B	0	# 0237-0	UB_TJ300

END

UB_SPS_CAGE 2

# Slot	Type	Serial #	Code	Num	Date	Name
1	879-802-02	0x029e65e	F	0	# 9817-0	UB_SPS_802
2	949-700-10	0x029e84b	B	0	# 0104-0	UB_QVS_CAL 1
3	949-693-10	0x80306a6	B	0	# 0019-0	UB_QVS_CTRL 22 ??
4	949-698-10	0x02998ca	D	0	# 0317-0	UB_QVS_AM 1
5	949-698-10	0x02998c4	D	0	# 0317-0	UB_QVS_AM 2
6	949-698-10	0x02a1e97	D	0	# 0317-0	UB_QVS_AM 3
7	949-698-10	0x02998c6	D	0	# 0317-0	UB_QVS_AM 4
22	517-300-01	0x0283054	B	0	# 0237-0	UB_TJ300

END

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CSB_CAGE 8
#Slot Type      Serial #   Rev   Num   Fld1   Fld2   Date   Name
  1  949-920-60  0x8035516  C    0
  2  949-866-00  0x8034aa8  C    0
# 0323-0 HSD CSB
# 0051-0 SPLITTER
END

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TIGER_TH 1
BACKPLANE B
#Slot Type      Serial #   Rev   Num   Date   Name
 35  805-870-70  0x02911b7  B    0   # 0323-f PE32 128M
 36  805-870-70  0x0291670  B    0   # 0331-f PE32 128M
 37  805-870-70  0x02915f1  B    0   # 0323-f PE32 128M
 38  805-870-70  0x02912f9  B    0   # 0323-f PE32 128M
 39  805-873-50  0x029160d  B    0   # 0320-f QSB
 45  805-870-70  0x02915fe  B    0   # 0325-f PE32 128M
 46  805-870-70  0x02915b9  B    0   # 0325-f PE32 128M
 47  805-870-70  0x0290e88  B    0   # 0235-f PE32 128M
 48
 49  805-873-50  0x0291230  A    0   # 0320-f QSB
 50
 51
 52
 53
END

```

```

#
# Time Subsystem
#
TIME_SUBSYSTEM
#Slot Board ID   Serial #   Rev   Num   Date   Name
  X   949-782-00  0x02A2295  B    0   # 9829-0 Time Mux Board 1
  X
END

```

```

#
# DC Subsystem -
#
# SRC <NUM> [1 - 13]
# (sources 1-5 are MATRIX sources 1-5
# sources 6-13 are DUT sources 1-8)
# HCU <NUM> *[1 - 4]
# REF HCU <NUM> *[1 - 4]
# HVSRC <NUM> *[1 - 4]
# PWSRC <NUM> [1 - 4]
# DATABITS <NUM> - <NUM> [1 - 192]
#
# ** These instruments share the same seven-slot cage -- only one
# instrument is allowed per slot.
#

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DC_SUBSYSTEM

```
# UBVI 60 1 ( 60V V/I Source in Universal Backplane 1 : slot 14)
# UBVI 60 2 ( 60V V/I Source in Universal Backplane 1 : slot 16)
# UBVI 60 3 ( 60V V/I Source in Universal Backplane 1 : slot 17)
# UBVI 60 4 ( 60V V/I Source in Universal Backplane 1 : slot 18)
# UBVI 60 6 ( 60V V/I Source in Universal Backplane 1 : slot 15)
DATABITS 1 - 48
```

UB_MATRIX

```
#
# Testhead 1
# XPTs UB Cage Slot Type
# 1-4 1 2 APU
# 5-8 1 3 APU
# 9-12 1 4 APU
# 13-16 1 5 APU
# 17-20 1 6 APU
# 21-24 1 7 APU
# 25-28 1 8 APU
# 29-32 1 9 APU
# 33-36 1 10 APU
# 37-40 1 11 APU
# 41-44 1 12 APU
# 45-48 1 13 APU
```

END