

```
# Confsim file created on: 10/04/05 13:36:57
#
# Catalyst tester
#
# cat2 is a 2 processor system
#   Processor 1: 1280 MHz sparcv9 (online)
#   Processor 2: 1280 MHz sparcv9 (online)
# PCI based system
# Terabus is present
#   Board ID      949-824-01
#
# TCI is present
#
```

CATALYST_TH 1 BACKPLANE A

#Slot	Type	Serial #	Num	XptA	XptB	Name
2	000-000-00	0x00000000	0	# 23	24	EMPTY
3	000-000-00	0x00000000	0	# 21	22	EMPTY
4	000-000-00	0x00000000	0	# 19	20	EMPTY
5	879-792-01	0x2003c4a	0	# 17	18	TIME CC
6	000-000-00	0x00000000	0	# 15	16	EMPTY
7	949-681-50	0x303aec2	0	# 13	14	VHFAWG400 DIFF CC
8	949-672-50	0x300258d	0	# 11	12	VHFDIG CC
9	879-906-51	0x00043bb	0	# 9	10	VHFCW CC
10	949-681-50	0x303e546	0	# 7	8	VHFAWG400 DIFF CC
11	949-672-50	0x300259e	0	# 5	6	VHFDIG CC
14	803-594-00	0x000401a	0	# 25	26	UHFSRC CC
15	803-596-05	0x02221bd	0	# 27	28	UWPORT
16	803-595-00	0x025c67f	0	# 29	30	UWMM
17	000-000-00	0x00000000	0	# 31	32	EMPTY
18	803-596-05	0x02221fa	0	# 33	34	UWPORT
19	803-594-00	0x0003d8c	0	# 35	36	UHFSRC CC
20	000-000-00	0x00000000	0	# 37	38	EMPTY
21	000-000-00	0x00000000	0	# 39	40	EMPTY
22	000-000-00	0x00000000	0	# 41	42	EMPTY
23	000-000-00	0x00000000	0	# 43	44	EMPTY
1	949-669-00	0x200575b	0	# 3	0	HAS (Left HAS LA669-00)
13	949-668-00	0x2005723	0	# 0	0	CATALYST HAC
12	949-667-00	0x2005ac0	0	# 0	0	DIF
24	949-669-01	0x20054ad	0	# 4	0	HAS (Right HAS LA669-01)

END

RF_PIPES 1

#	pipe-name	slot	schan	description
	Z8A	15	1	# UWPORT (OSP)
	Z7A	15	2	# UWPORT (OSP)
	Z6A	15	3	# UWPORT (OSP)
	Z5A	15	4	# UWPORT (OSP)
	Z4A	18	1	# UWPORT (OSP)
	Z3A	18	2	# UWPORT (OSP)
	Z2A	18	3	# UWPORT (OSP)
	Z1A	18	4	# UWPORT (OSP)

END

RF_CONNECTIONS 1

RF pipe connections between channel cards

#	slot	schan	to	slot	schan
	14	1	to	15	5

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16      1      to      18      7
16      3      to      15      7
19      1      to      18      5

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END

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#
# Up to 4 Precision AC Card Cages are allowed
#

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PRECISION_AC 1
#Slot Type      Serial #      Num      Name
1  949-664-00    0x3040bdc  0      # VHFDIG MF
2  949-664-00    0x3040c19  0      # VHFDIG MF
3  949-827-00    0x303b245  0      # VHFAWG
4  949-320-00    0x00028b1  0      # UWMS
5  949-827-00    0x303b25a  0      # VHFAWG
6  949-320-00    0x0279af8  0      # UWMS
7  949-320-00    0x0012247  0      # UWMS
8  949-671-01    0x3002935  0      # PACS CAGE INT

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END

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#
# Up to 8 Universal Backplane/Synch Power Subsystem
# cages are allowed
#

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For the Synch Power Subsystem:

```

# Slot      Type      Name      Instr1      #      Instr2      #      Ammeter      #
#
# Instr1 # - instrument connected to the first two matrix lines
# Instr2 # - instrument connected to the last two matrix lines
# Ammeter # - ammeter connection
# to AVOID errors, put NO 0 if no instrument is connected.
#
#

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```

UB_SPS_CAGE 1
# Slot      Type      Serial #      Num      Name
1  879-802-02    0x2005148  0      # UB_SPS_802
2  517-301-00    0x000ae73  0      # UB_APU
3  517-301-00    0x000ae7d  0      # UB_APU
4  517-301-00    0x000aea9  0      # UB_APU
5  517-301-00    0x000ae77  0      # UB_APU
6  517-301-00    0x000ae74  0      # UB_APU
7  517-301-00    0x000ae7c  0      # UB_APU
8  517-301-00    0x000ae86  0      # UB_APU
9  517-301-00    0x000ae72  0      # UB_APU
10 517-301-00    0x026036d  0      # UB_APU
11 517-301-00    0x000ae8f  0      # UB_APU
12 517-301-00    0x000ae6d  0      # UB_APU
13 517-301-00    0x000ae80  0      # UB_APU
14 879-925-01    0x0205704  0      # UB_60_V_SRC MAT 1
15 879-925-01    0x303faa1  0      # UB_60_V_SRC DUT 1
16 879-925-01    0x30400d3  0      # UB_60_V_SRC DUT 5
17 879-925-01    0x303fa92  0      # UB_60_V_SRC DUT 6
18 879-925-01    0x3040efd  0      # UB_60_V_SRC DUT 7
19 879-925-01    0x3002985  0      # UB_60_V_SRC MAT 2
21 879-690-00    0x2005605  0      # UB_ASY
22 517-300-01    0x000d058  0      # UB_TJ300

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END

HSD100_CHAN_CAGE 1

#Slot	Type	Serial #	Num	Fld1	Fld2	Name
1	949-921-01	0x303feae	0			# HSD CDM 400
2	949-921-01	0x304165e	0			# HSD CDM 400
3	949-921-01	0x30416b3	0			# HSD CDM 400
4	949-921-01	0x30416b0	0			# HSD CDM 400
5	949-920-10	0x303b135	0			# HSD CSB

END

CATALYST_TH 1 BACKPLANE B

#Slot	Type	Serial #	Num	Name
47	949-626-10	0x3041a13	0	# HSD THS
61	949-625-00	0x118176f	0	# HSD DTH
62	949-625-00	0x8015f33	0	# HSD DTH
63	949-625-00	0x800d17c	0	# HSD DTH
64	949-625-00	0x119aae9	0	# HSD DTH
65	949-626-00	0x3038dc6	0	# HSD THS
66	949-625-00	0x303ff9a	0	# HSD DTH
67	949-625-00	0x11c1956	0	# HSD DTH
68	949-625-00	0x303ff6b	0	# HSD DTH
69	949-625-00	0x8010e24	0	# HSD DTH

END

Trigger Switch Yard

Note: The logical slot numbers below correspond to the physical slot
numbers only for test systems which contain a TSY card
cage. In test systems which contain an SCS/TSY card cage the
mapping is:

Logical TSY slot (below)	Physical slot
1	-> 2
3	-> 1

TSY CAGE

#Slot	Type	Serial #	Num	Name
1	879-655-02	0x2005867	0	# TSY
2	000-000-00	0xffffffff	0	# EMPTY
3	000-000-00	0xffffffff	0	# EMPTY
4	000-000-00	0xffffffff	0	# EMPTY

END

Time Subsystem

TIME SUBSYSTEM

#	Board ID	Serial #	Name
	879-793-00	0x303FE53	# TMS Timer
	879-794-01	0x20056E3	# TMS Counter
	879-795-01	0x2003DF0	# TMS Support

END

The UWMS option resides in the PACS cage and is composed of two
plug-in modules that may reside in one of three locations A, B or C.
A zero in the type field indicates no plug-in module.
PACS_UW <n> - indicates PACS cage number of these UWMS options
Slot - indicates slot number and module position for this UWMS option
Type - board identification number


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# AWG Num - AWG instrument number connected to this UWMS option
# UHFSRC Num - UHF instrument number that has this UWMS option
# MODSRC Num - First or second MODSRC connected to above UHFSRC
#
PACS_UW 1
#
#Slot Type          Serial #  Rev    Date      AWG  UHFSRC  MODSRC  Name
#          4 949-320-00 0x00028b1 A    0226-0    1      1      1      # UWMS
#          4A 949-330-20 0x02a02a3 A    0312-0                                # MODSRC6000 UP CONV
#          4B 000-000-00 0x0000000 0    0000-0                                # MODSRC6000 MUX
#          4C 939-204-00 0x00010ad A    0331-0                                # MODSRC6000 MUX
END
#
#
# The UWPORT option modules are composed of plug-in modules.
# The names list in the first column indicate the presence of
# the option modules for the uwport.
#
#
# DC Subsystem -
#
# SRC <NUM> [1 - 13]
# (sources 1-5 are MATRIX sources 1-5
# sources 6-13 are DUT sources 1-8)
# HCU <NUM> *[1 - 4]
# REF HCU <NUM> *[1 - 4]
# HVSRC <NUM> *[1 - 4]
# PWSRC <NUM> [1 - 4]
# DATABITS <NUM> - <NUM> [1 - 192]
#
# ** These instruments share the same seven-slot cage -- only one
# instrument is allowed per slot.
#
DC_SUBSYSTEM
# UBVI 60 1 ( 60V V/I Source in Universal Backplane 1 : slot 14)
# UBVI 60 2 ( 60V V/I Source in Universal Backplane 1 : slot 19)
# SRC 5
# UBVI 60 6 ( 60V V/I Source in Universal Backplane 1 : slot 15)
# UBVI 60 10 ( 60V V/I Source in Universal Backplane 1 : slot 16)
# UBVI 60 11 ( 60V V/I Source in Universal Backplane 1 : slot 17)
# UBVI 60 12 ( 60V V/I Source in Universal Backplane 1 : slot 18)
# DATABITS 1 - 48
#
# UB_MATRIX
#
# Testhead 1
# XPTs UB Cage Slot Type
# 1-4 1 2 APU
# 5-8 1 3 APU
# 9-12 1 4 APU
# 13-16 1 5 APU
# 17-20 1 6 APU
# 21-24 1 7 APU
# 25-28 1 8 APU
# 29-32 1 9 APU
# 33-36 1 10 APU
# 37-40 1 11 APU
# 41-44 1 12 APU
# 45-48 1 13 APU
END

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